

Lecture		ATK T5000 Memory ATL/MCI Programming Basic		
System		T5800/T5500/T5300 platform		
Day	Time		Item	Duration (H)
	Start	End		
Day1	9:30	11:30	HW Overview	
	11:30	13:00	Lunch Break	1:30
	13:00	17:00	SW Overview (LAB-1) Socket program & Rejection/Exclusion mode DC Parametric Test DC Input Leak Test (LAB-1a)	4:00
Day2	9:30	11:30	Power Supply Driver Channel setting (Level, Timing, Waveform)	2:00
	11:30	13:00	Lunch Break	1:30
	13:00	17:00	Driver Channel setting (LAB-2) IO Channel setting (Level, Timing, Waveform, Strb, DRE)	4:00
Day3	9:30	11:30	Functional Test	2:00
	11:30	13:00	Lunch Break	1:30
	13:00	17:00	Functional Test (LAB-3) PDS & Cycle Palette PDS & Cycle Palette (LAB-4)	4:00
Day4	9:30	11:30	Pattern programming	2:00
	11:30	13:00	Lunch Break	1:30
	13:00	17:00	Pattern programming (LAB-5) Interleave pattern programming Interleave pattern programming (LAB-6)	4:00